

*ABSTRACT AMENDMENT*

Replace the Abstract with:

Voltages of nodes are controlled in accordance with an input signal using inverters. A voltage level of a transistor is adjusted using a transistor included in the inverter. By setting a gate voltage supplied to a gate of the transistor to be lower than a power supply voltage and higher than an ON-state voltage, a gate ~~leak~~ leakage current of the transistor can be greatly decreased.